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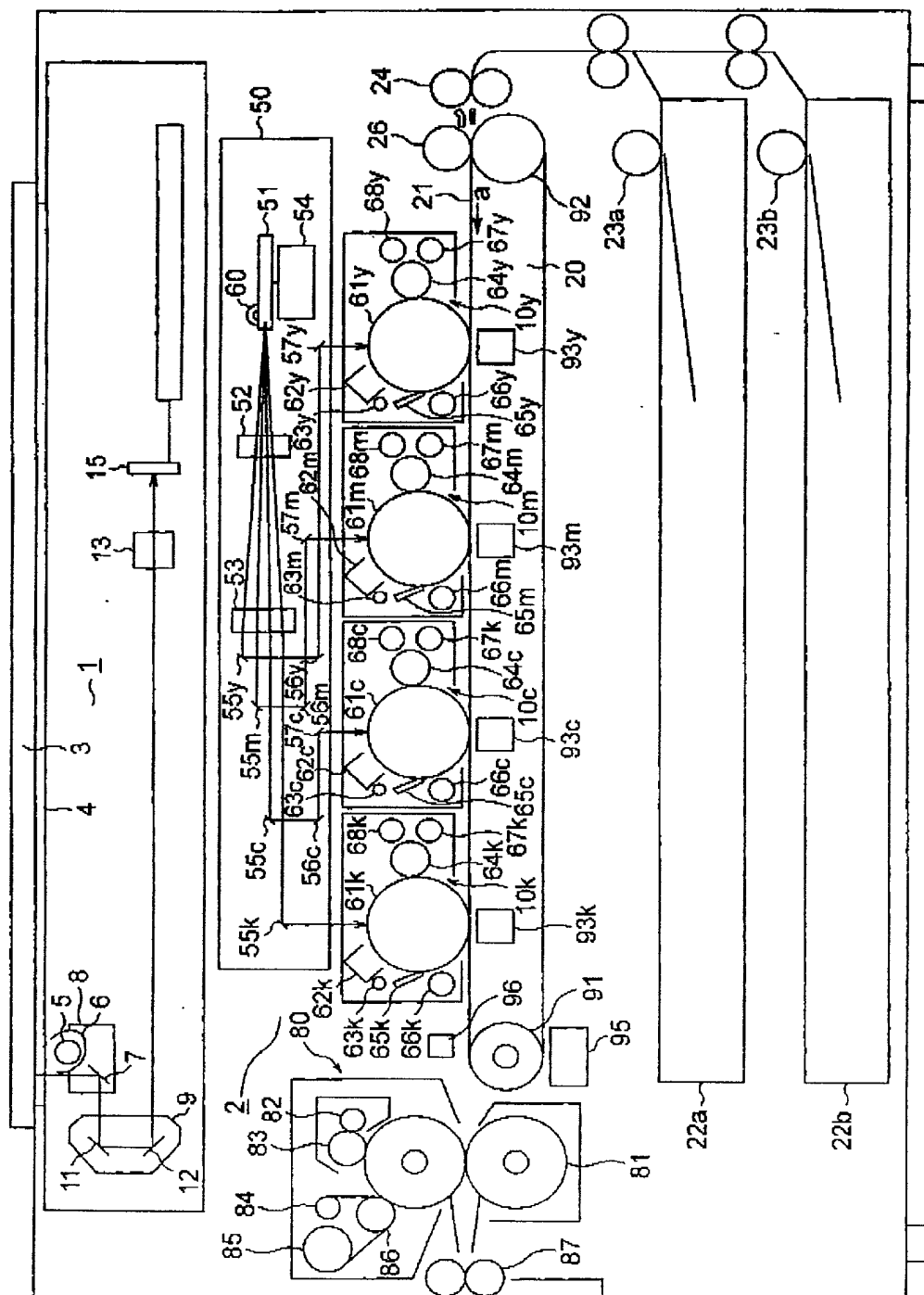


FIG. 1

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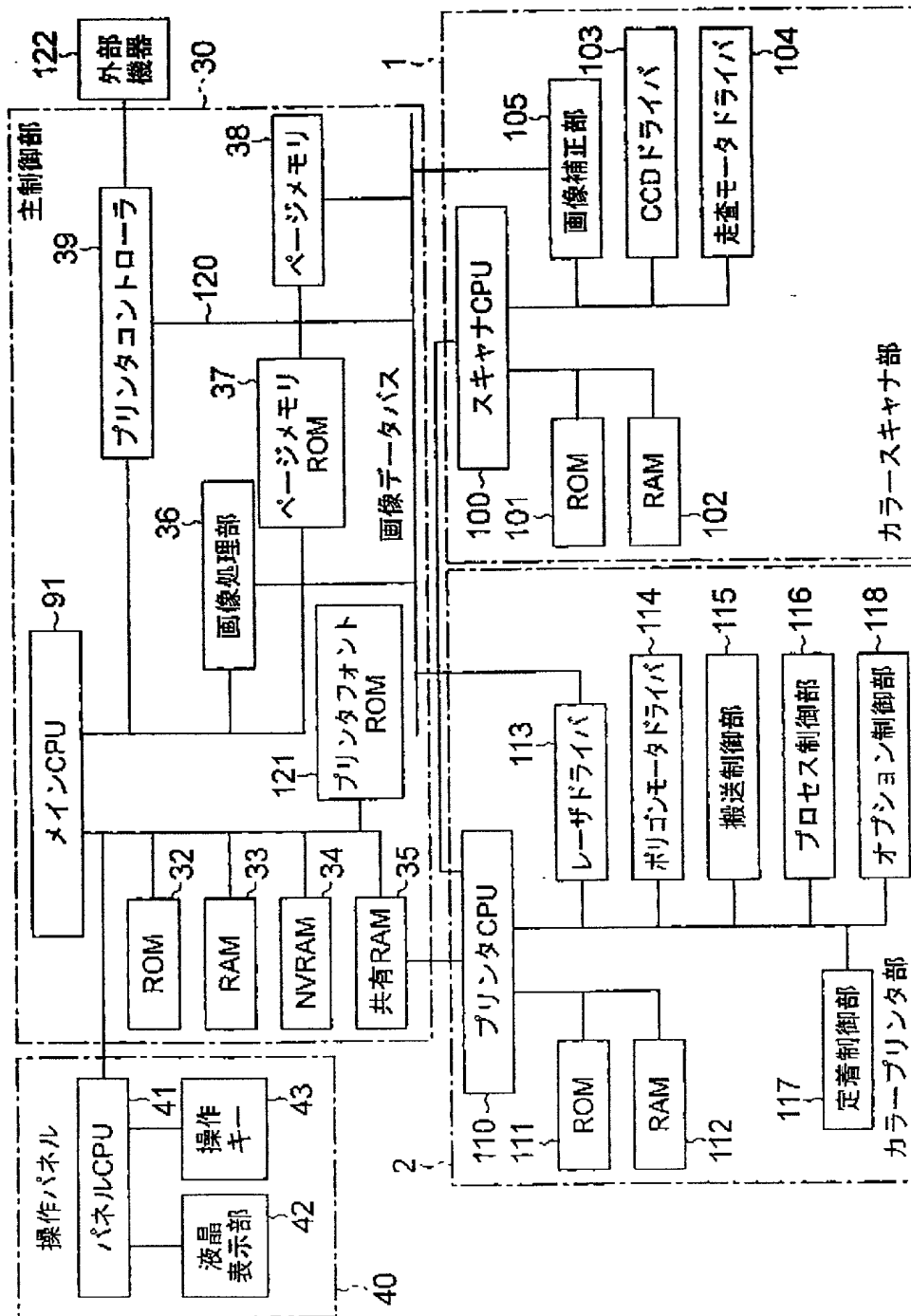


FIG. 2

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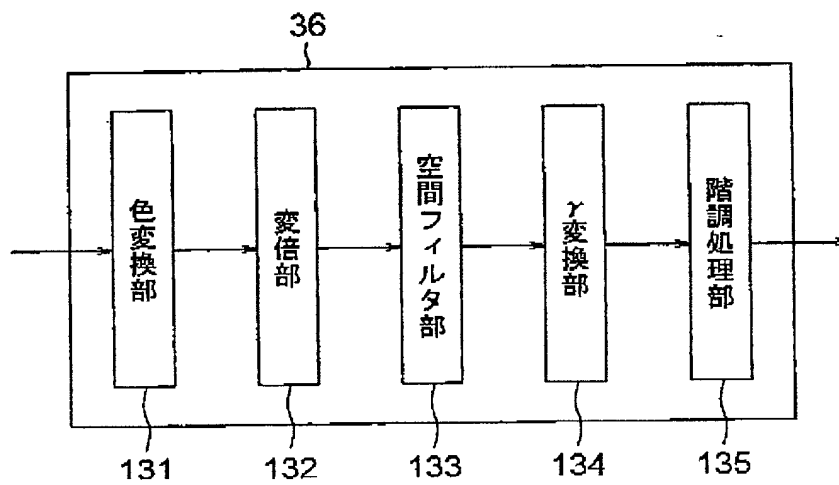


FIG. 3

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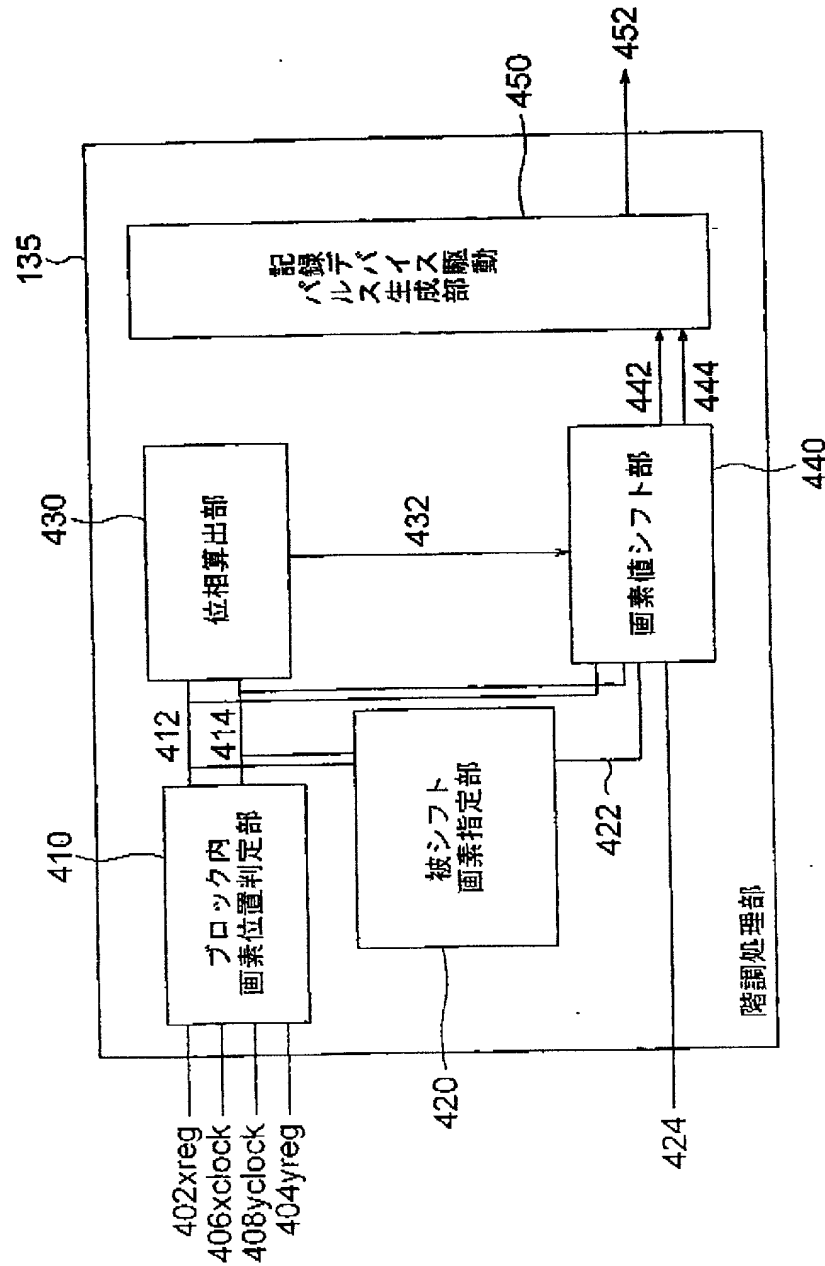


FIG. 4

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基準位置=右基準

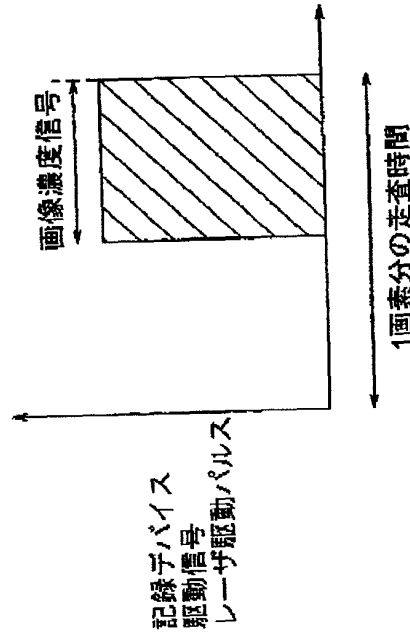


FIG. 5B

基準位置=左基準

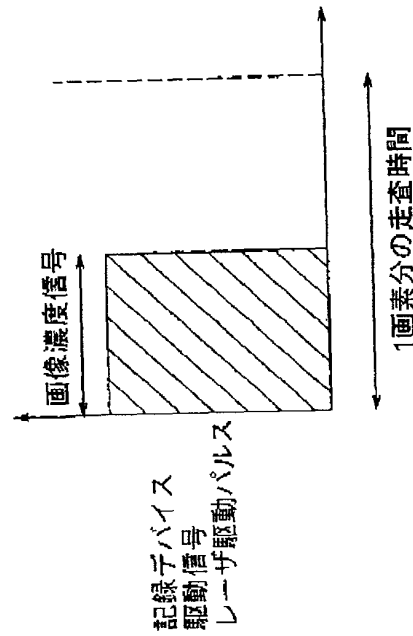


FIG. 5A

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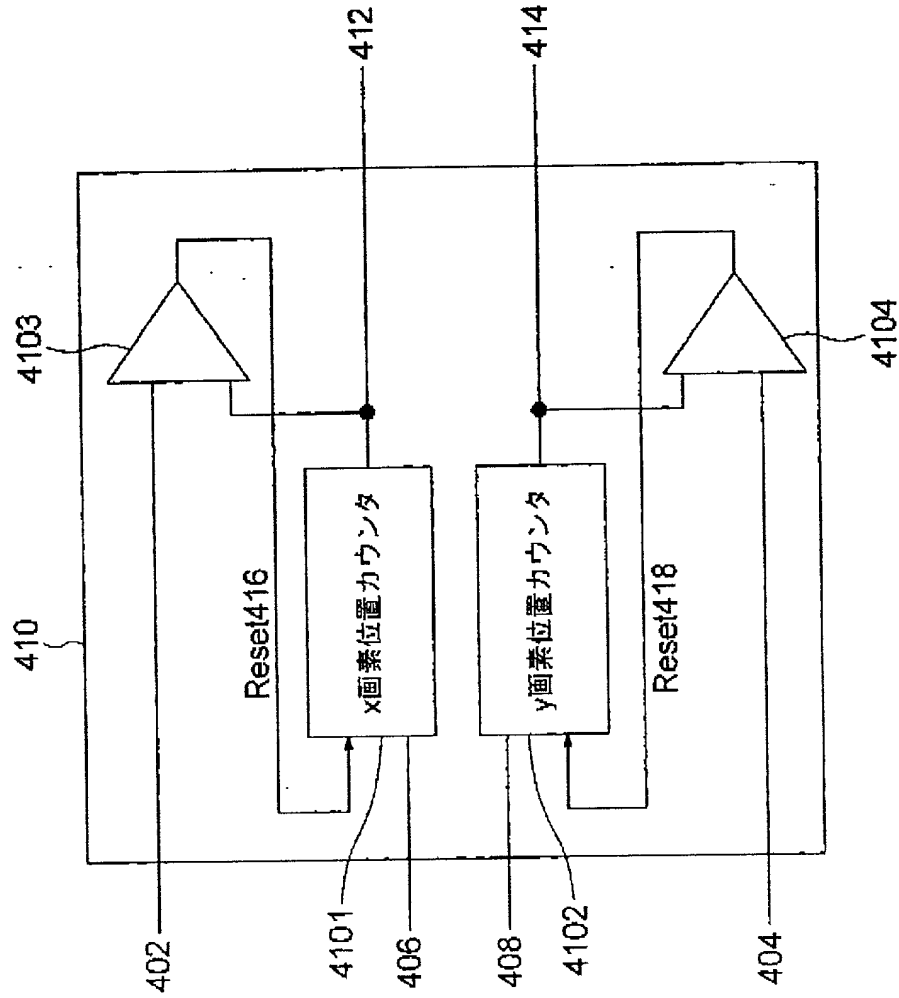


FIG. 6

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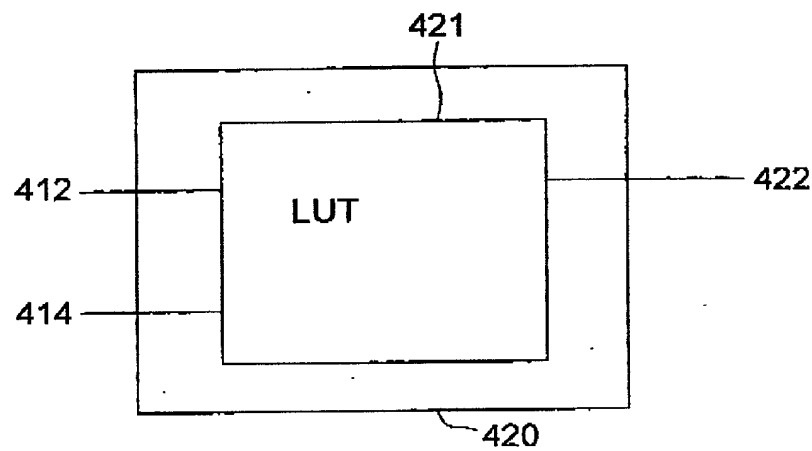


FIG. 7

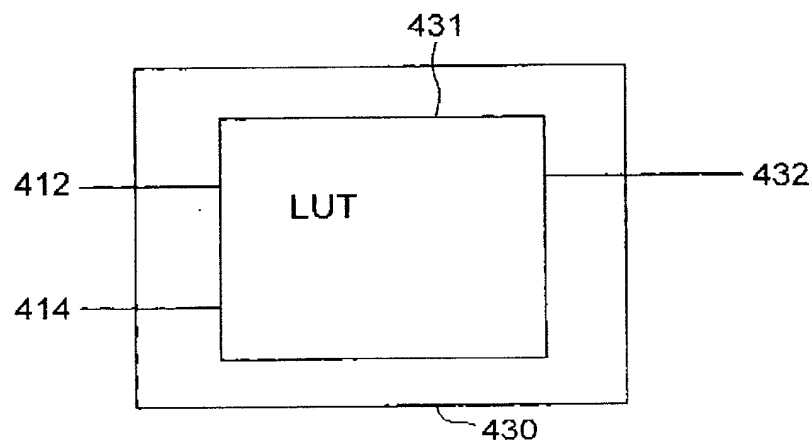


FIG. 8

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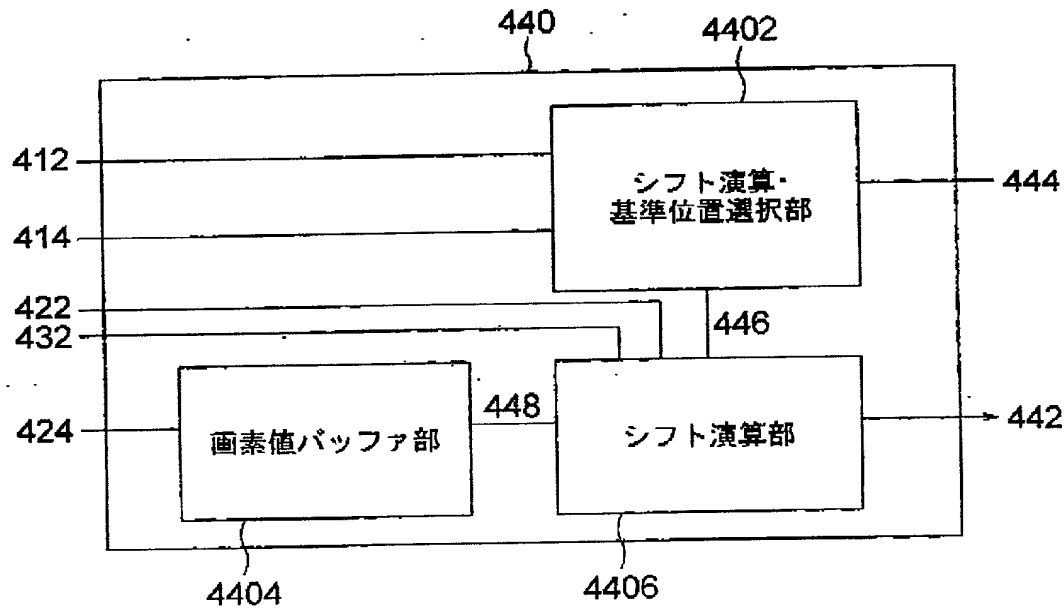


FIG. 9

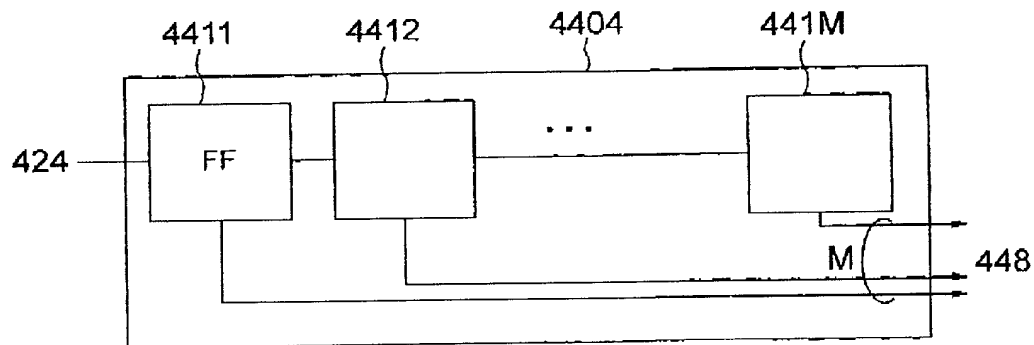


FIG. 10

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FIG. 11

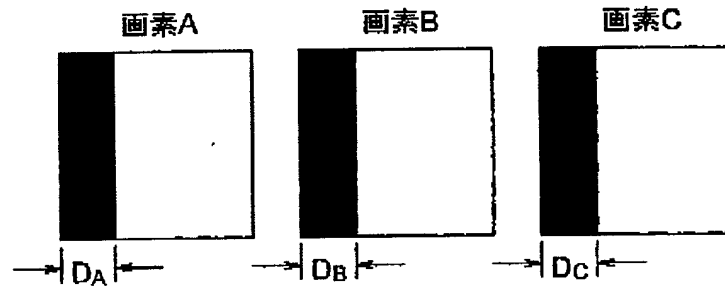


FIG. 12

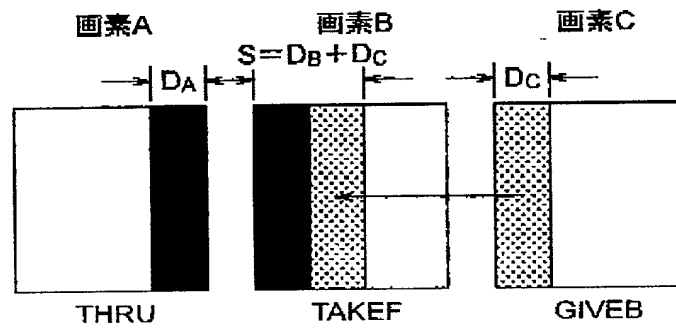


FIG. 13A

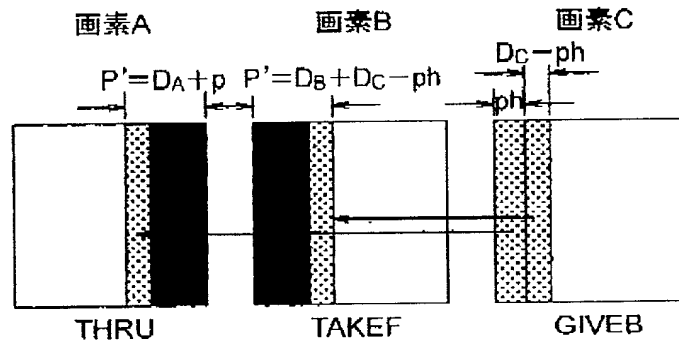
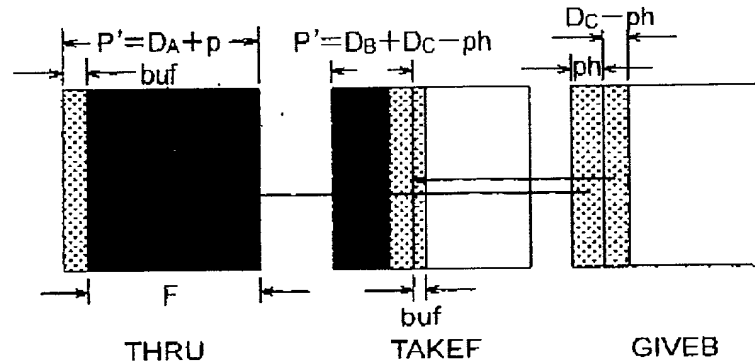


FIG. 13B



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FIG. 14

演算名	処理画素の画素値	書込シフトバツファ値	条件
THRU	0	P	$P_h \geq 0$ かつ $Pf_1 + Pf_2 + P \geq xff \times 2$
	$Pf_1 + Pf_2 + P - xff \times 2$	$xff \times 2 - Pf_1 + Pf_2$	$P_h \geq 0$ かつ $Pf_1 + Pf_2 + P < xff \times 2$
	$P + P_h$	P_h	$P_h < 0$ かつ $Pf_1 + Pf_2 \geq -P_h$
	$P + P_1 + P_2$	$-P_h - (Pf_1 + Pf_2)$	$P_h < 0$ かつ $Pf_1 + Pf_2 < -P_h$
TAKEF	xff	$P + Pf + Pbuf - xff$	$P + Pf + Pbuf \geq xff$
	$P + Pf + Pbuf$	0	$P + Pf + Pbuf < xff$
GIVEB	Pbuf	0	

FIG. 17

演算名	処理画素の画素値	書込シフトバツファ値	条件
THRU	P	0	$P_h \geq 0$ かつ $Pf_1 + Pf_2 + P \geq xff \times 2$
TAKEF	xff	$P + Pf - xff$	$P + Pf \geq xff$
	$P + Pf$	0	$P + Pf < xff$
GIVEB	Pbuf	0	

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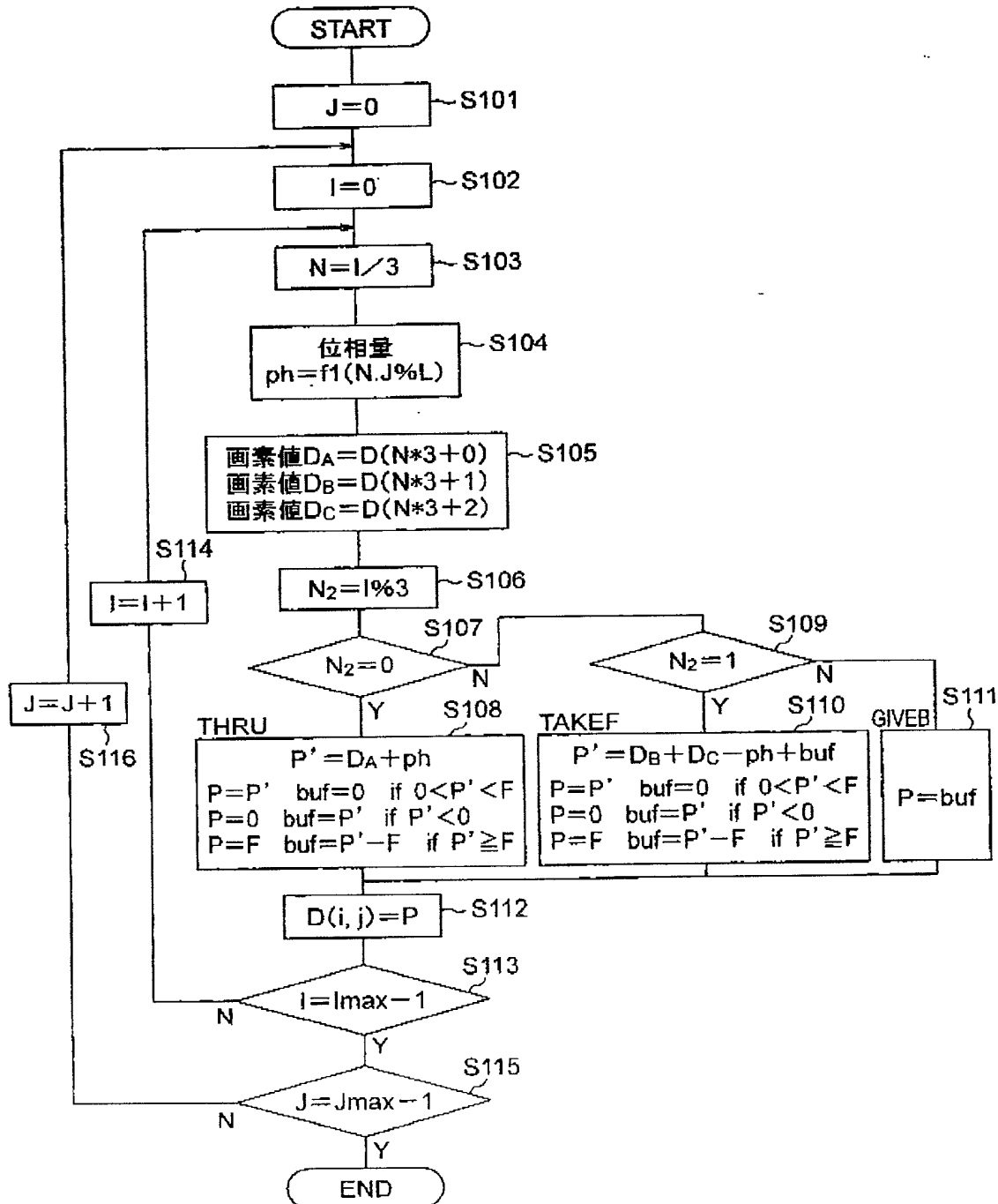


FIG. 15

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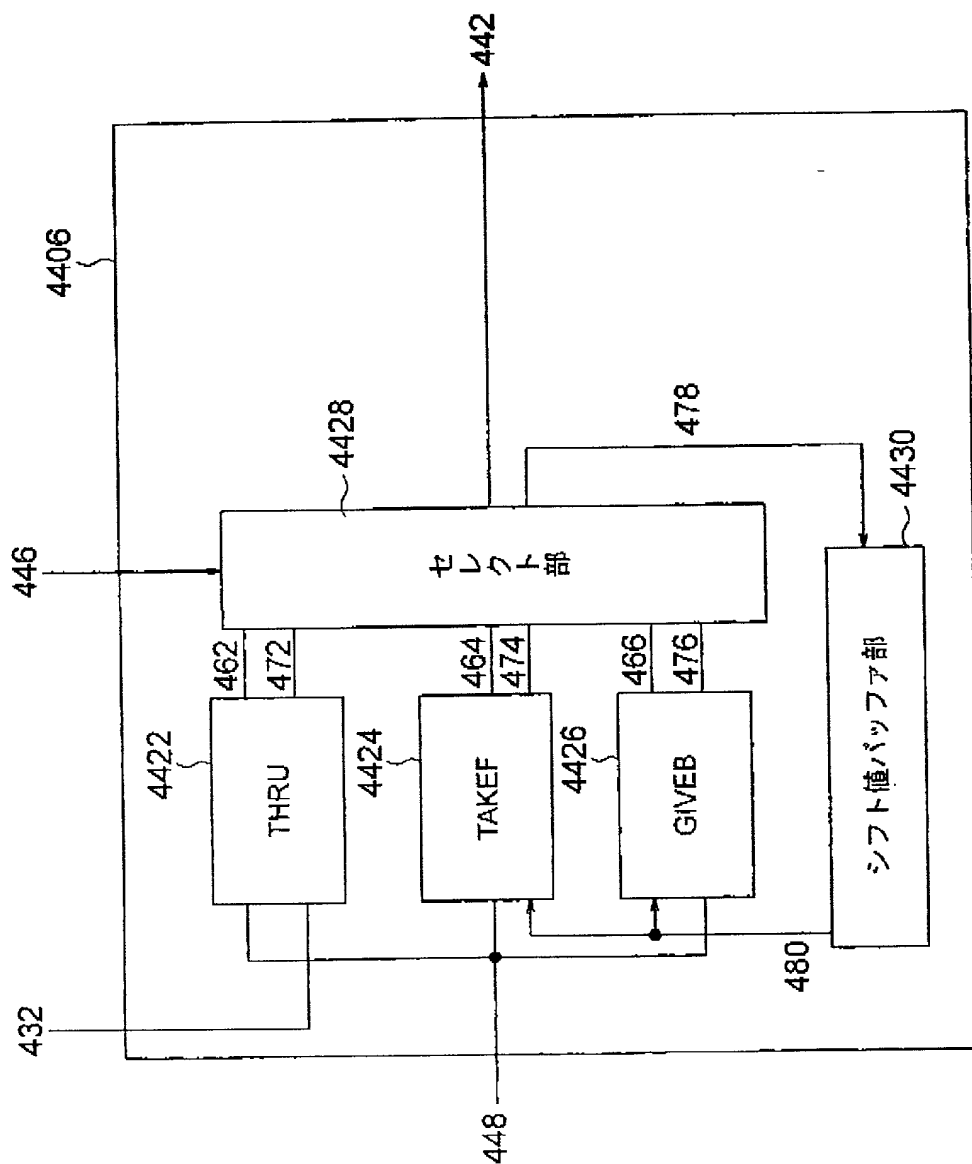


FIG. 16

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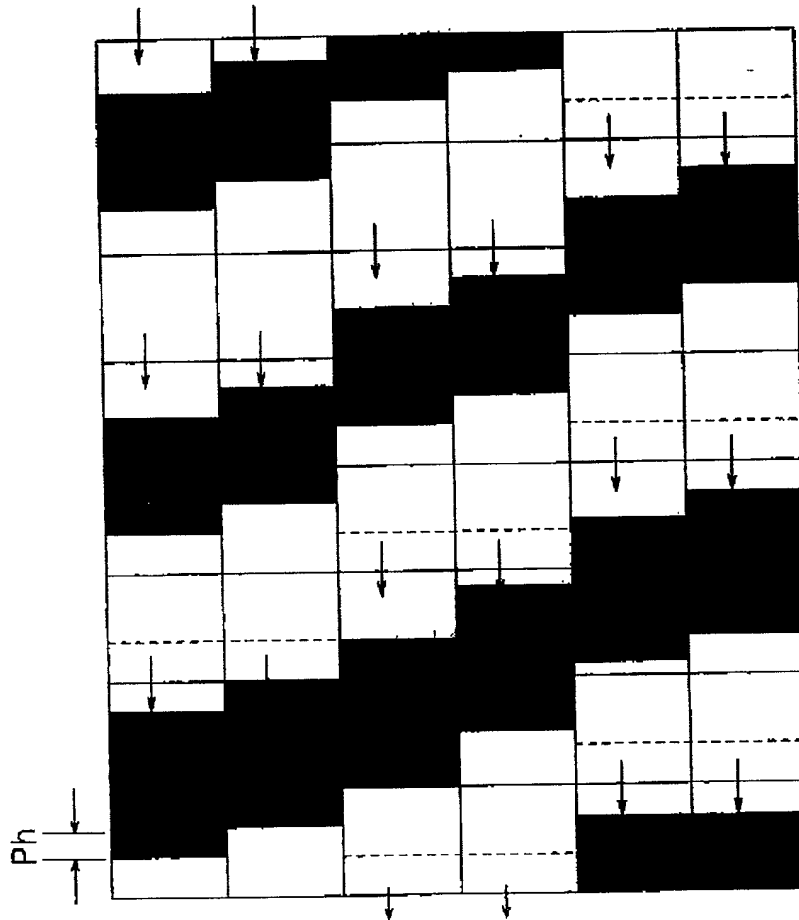


FIG. 18

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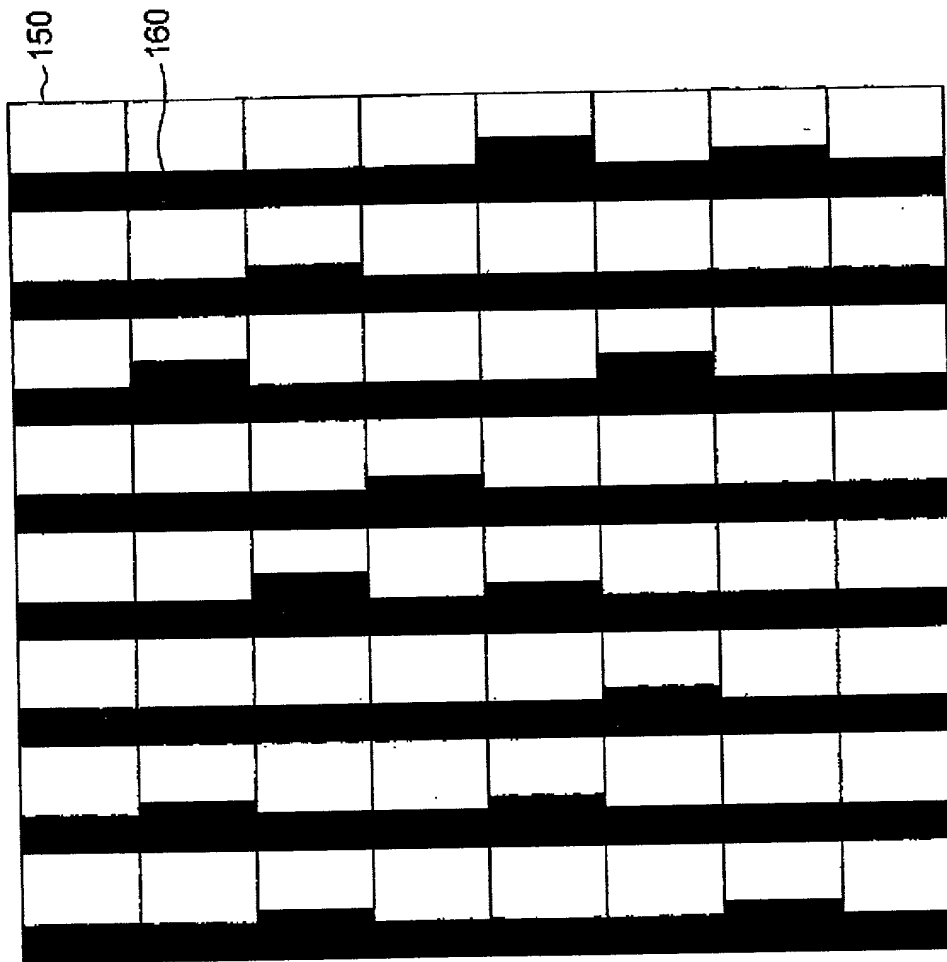


FIG. 19

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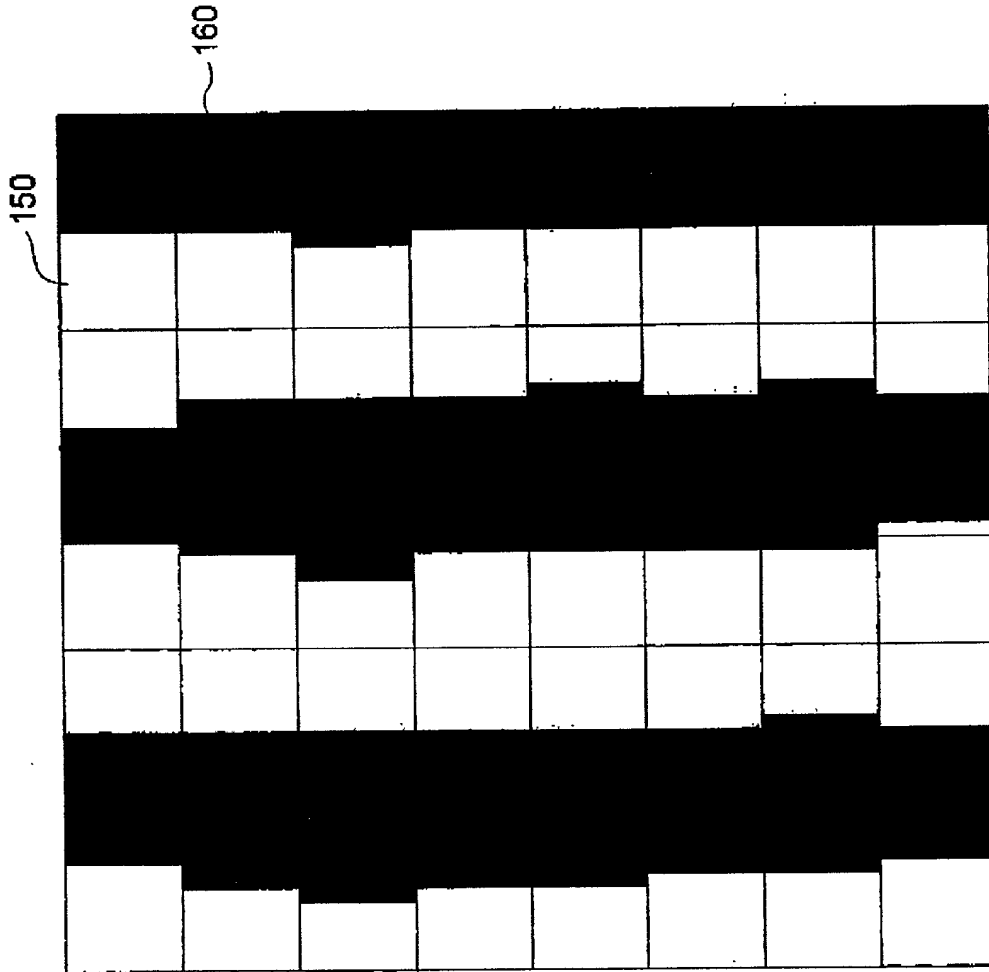


FIG. 20

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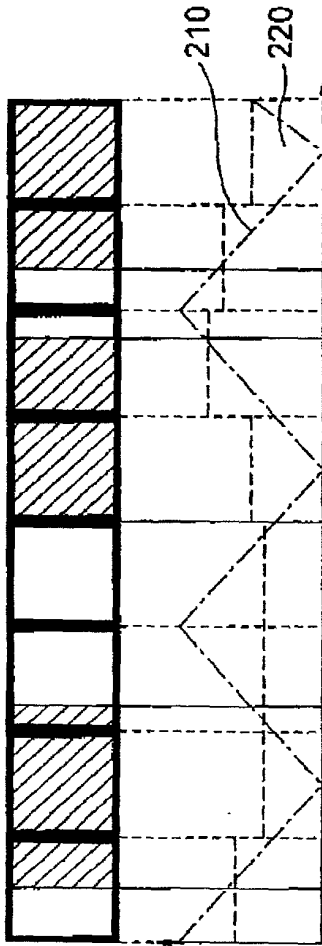
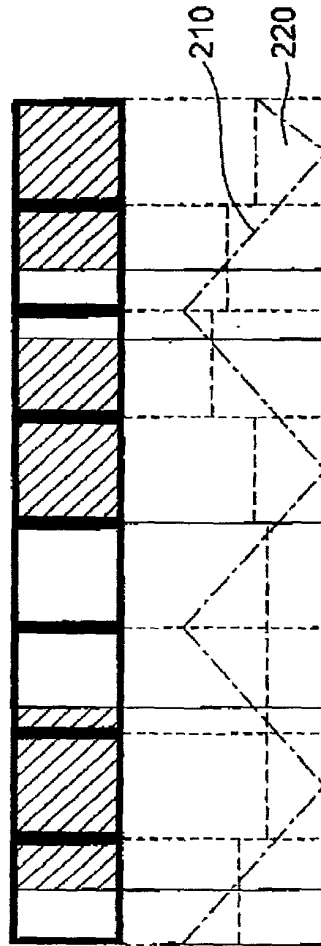
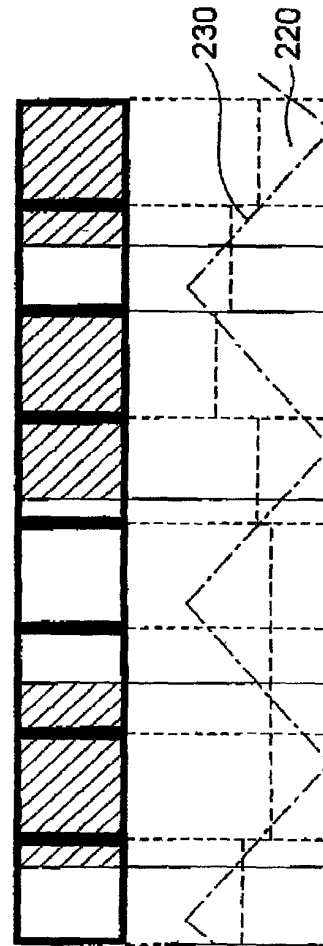


FIG. 21



1行目

FIG. 22A



2行目

FIG. 22B

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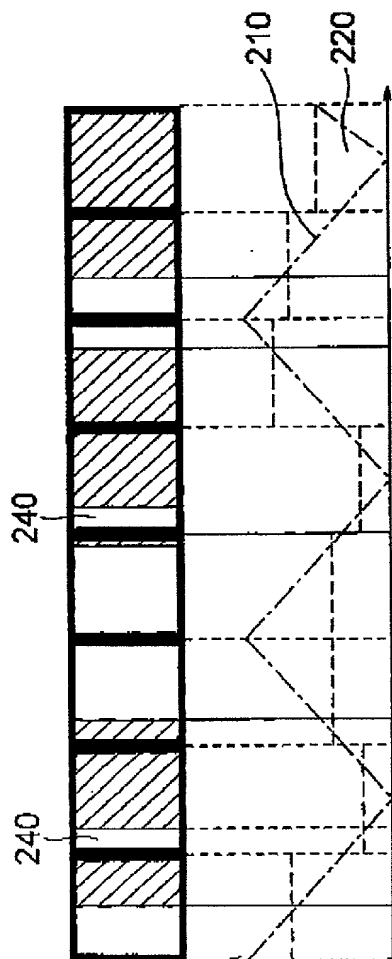


FIG. 23

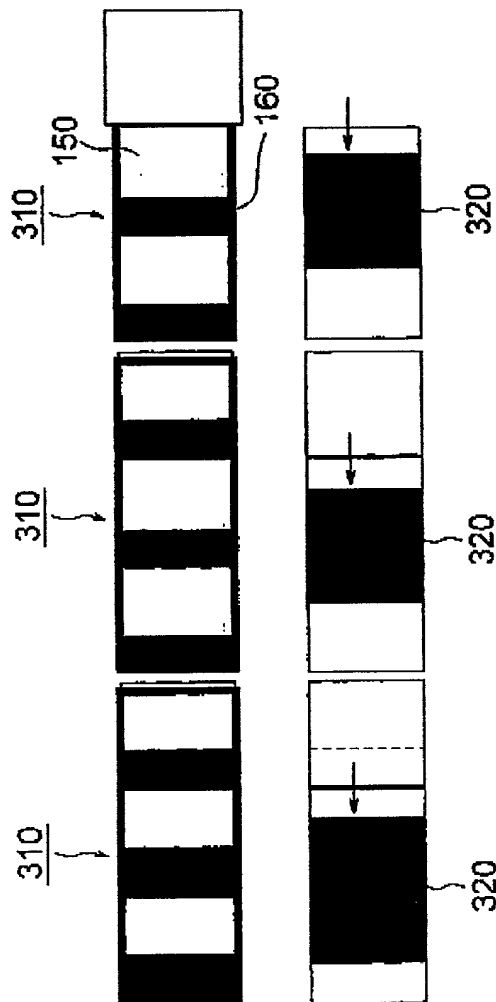


FIG. 24

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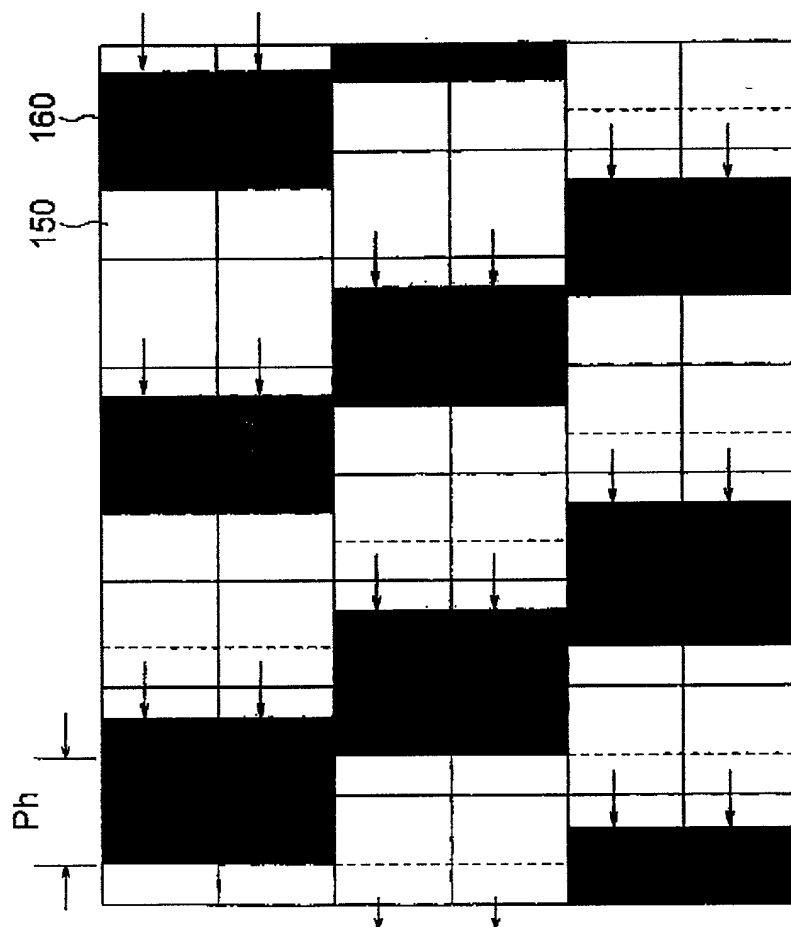


FIG. 25

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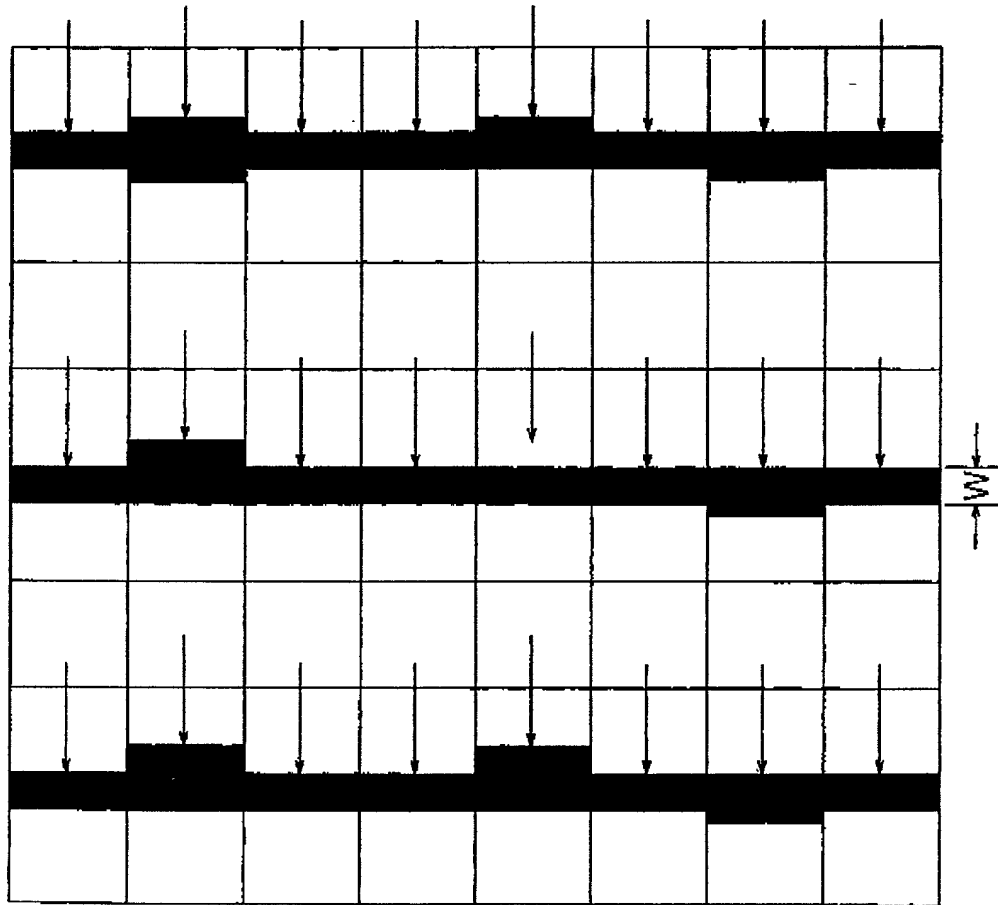


FIG. 26

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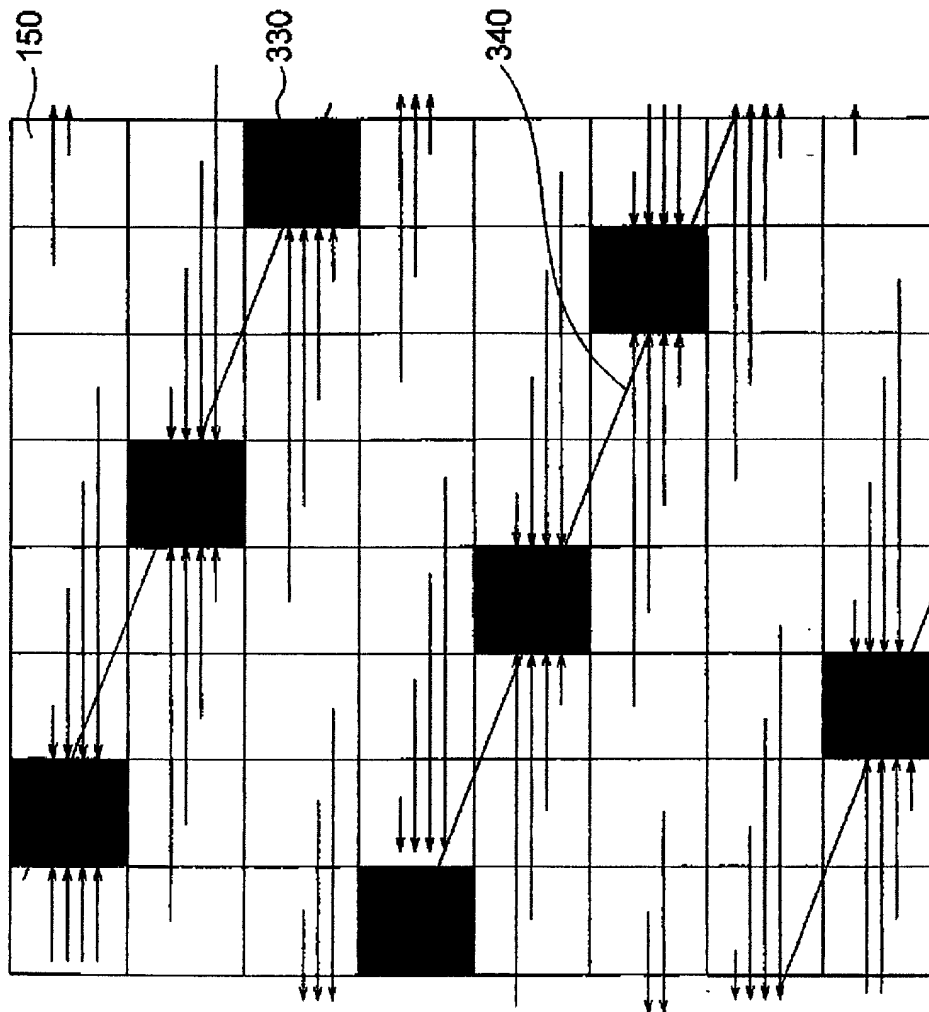


FIG. 27

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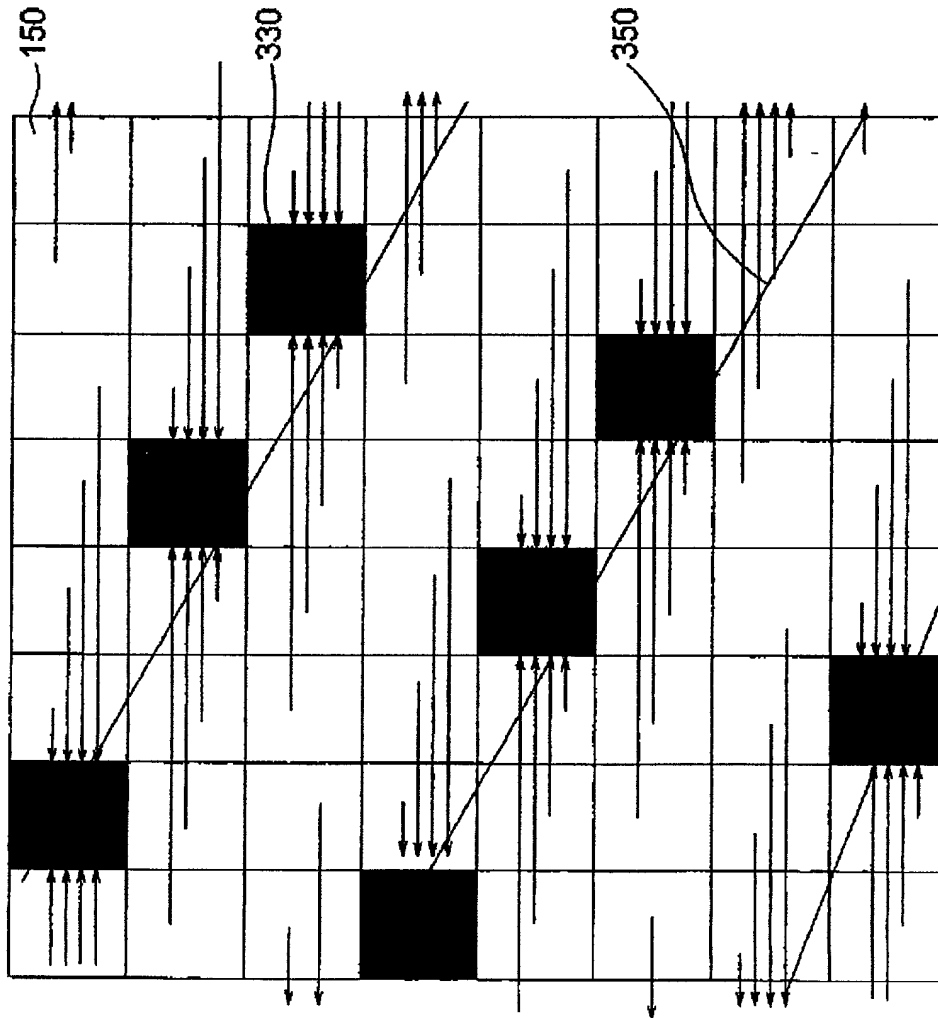


FIG. 28